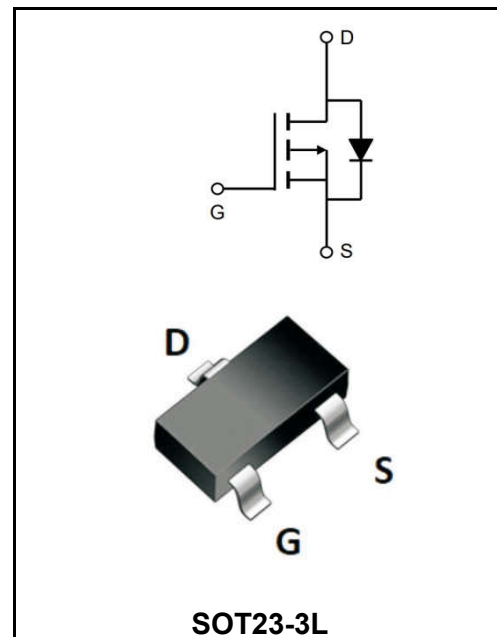


-55V P-CHANNEL ENHANCEMENT MODE MOSFET

MAIN CHARACTERISTICS

I_D	-4.2A
V_{DSS}	-55V
$R_{DS(on)}-typ(@V_{GS}=-10V)$	< 125mΩ (Type:108 mΩ)



Marking Code	
YFW4P05MI	4P05

Application

- ◆ Battery protection
- ◆ Load switch
- ◆ Uninterruptible power supply

Maximum Ratings at Tc=25°C unless otherwise specified

Characteristics	Symbols	Value	Units
Drain-Source Voltage	V_{DS}	-55	V
Gate - Source Voltage	V_{GS}	±20	V
Continuous Drain Current, V_{GS} @ -10V ¹ @T _A =25°C	I_D	-4.2	A
Continuous Drain Current, V_{GS} @ -10V ¹ @T _A =70°C	I_D	-2.4	A
Pulsed Drain Current ²	I_{DM}	-16	A
Total Power Dissipation ³ @T _A =25°C	P_D	1	W
Storage Temperature Range	T_{STG}	-55 to +150	°C
Operating Junction Temperature Range	T_J	-55 to +150	°C
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	125	°C/W
Thermal Resistance Junction to Case ¹	$R_{\theta JC}$	80	°C/W

Maximum Ratings at Tc=25°C unless otherwise specified

Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	BV_{DSS}	-55	-58	-	V
BV_{DSS} Temperature Coefficient	Reference to 25°C, $I_D=-1mA$	$\Delta BV_{DSS}/\Delta T_J$	-	-0.021	-	V/°C
Static Drain-Source On-Resistance ²	$V_{GS}=-10V, I_D=-1.5A$	$R_{DS(ON)}$	-	108	125	mΩ
	$V_{GS}=-4.5V, I_D=-1A$		-	125	155	
Gate -Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	$V_{GS(th)}$	-1.0	1.6	-2.5	V
$V_{GS(th)}$ Temperature Coefficient		$\Delta V_{GS(th)}$	-	4.08	-	mV/°C
Drain-Source Leakage Current	$V_{DS}=-48V, V_{GS}=0V, T_J=25^\circ C$	I_{DSS}	-	-	1	μA
	$V_{DS}=-48V, V_{GS}=0V, T_J=55^\circ C$		-	-	5	
Gate -Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	I_{GSS}	-	-	±100	nA
Forward Transconductance	$V_{DS}=-5V, I_D=-1.5V$	g_{fs}	-	5.9	-	S
Total Gate Charge(-4.5V)	$V_{DS}=-20V$ $V_{GS}=-4.5V$ $I_D=-1.5A$	Q_g	-	4.6	-	nC
Gate-Source Charge		Q_{gs}	-	1.4	-	
Gate-Drain Charge		Q_{gd}	-	1.62	-	
Turn-on delay time	$V_{DS}=-15V$ $V_{GS}=-10V$ $I_D=-1A$ $R_G=3.3\Omega$	$t_{d(on)}$	-	17.4	-	ns
Rise Time		T_r	-	5.4	-	
Turn-Off Delay Time		$t_{d(OFF)}$	-	37.2	-	
Fall Time		t_f	-	2.4	-	
Input Capacitance	$V_{DS}=-15V$ $V_{GS}=0V$ $f=1MHz$	C_{iss}	-	531	-	pF
Output Capacitance		C_{oss}	-	59	-	
Reverse Transfer Capacitance		C_{rss}	-	38	-	
Continuous Source Current ^{1,4}	$V_G=V_D=0V$, Force Current	I_S	-	-	-1.7	A
Pulsed Source Current ^{2,4}		I_{SM}	-	-	-7	A
Diode Forward Voltage ²	$V_{GS}=0V, I_S=-1A, T_J=25^\circ C$	V_{SD}	-	-	-1.2	V

Note :

- 1、The data tested by surface mounted on a 1 inch 2 FR-4 board with 20Z copper.
- 2、The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3、The power dissipation is limited by 150°C junction temperature
- 4、The data is theoretically the same as I D and I DM , in real applications , should be limited by total power dissipation.

Ratings and Characteristic Curves

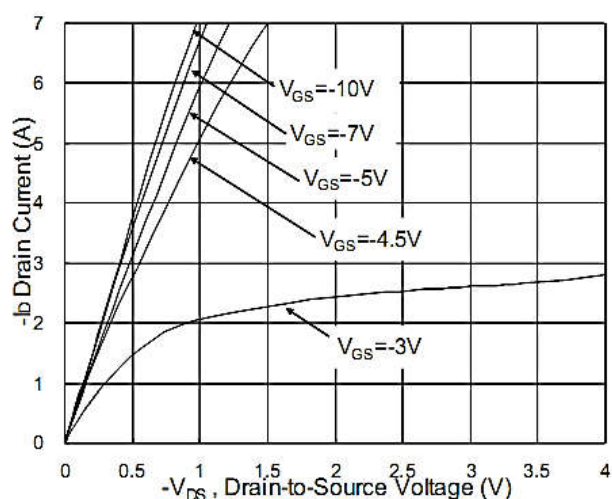


Fig.1 Typical Output Characteristics

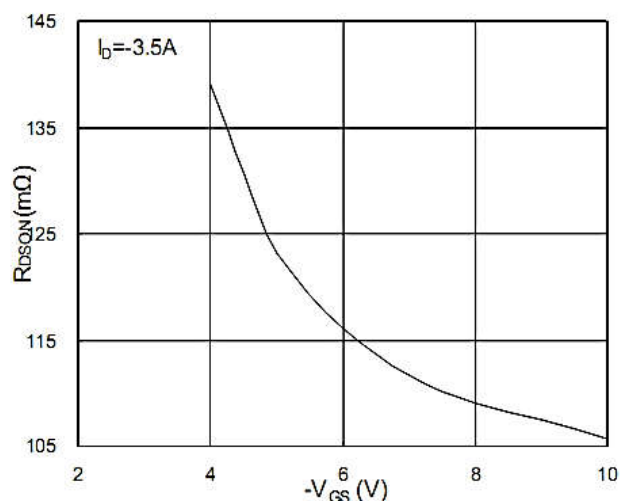


Fig.2 On-Resistance v.s Gate-Source

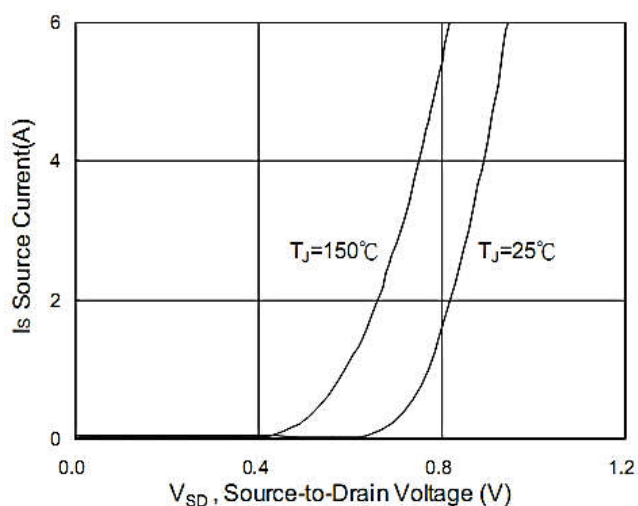


Fig.3 Forward Characteristics Of Reverse

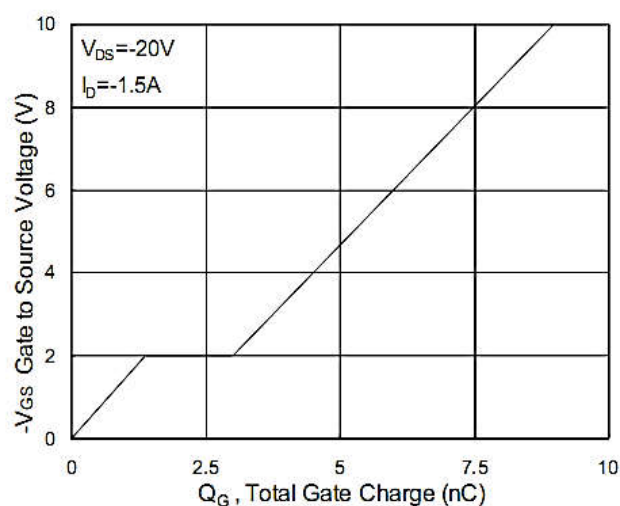


Fig.4 Gate-Charge Characteristics

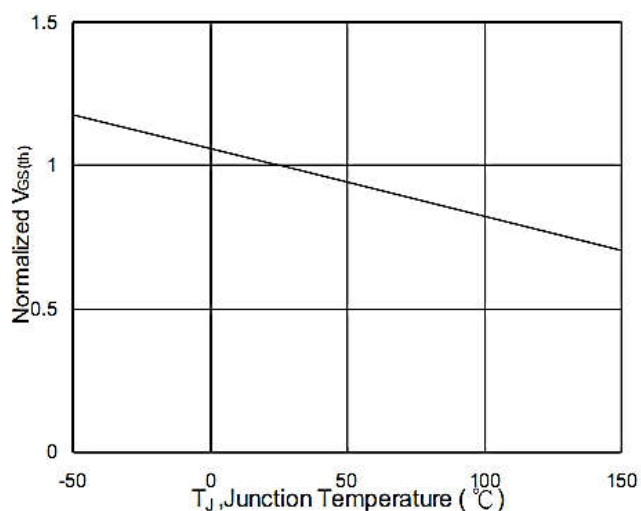


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

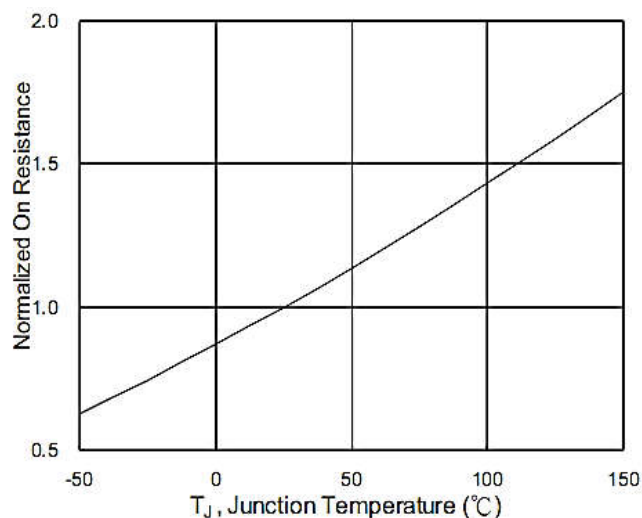


Fig.6 Normalized $R_{DS(on)}$ v.s T_J

Ratings and Characteristic Curves

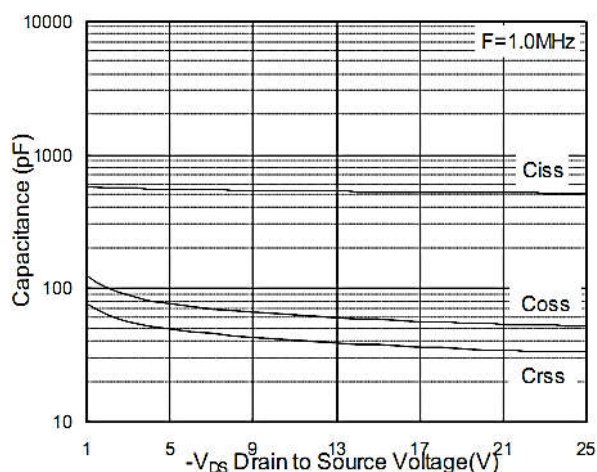


Fig.7 Capacitance

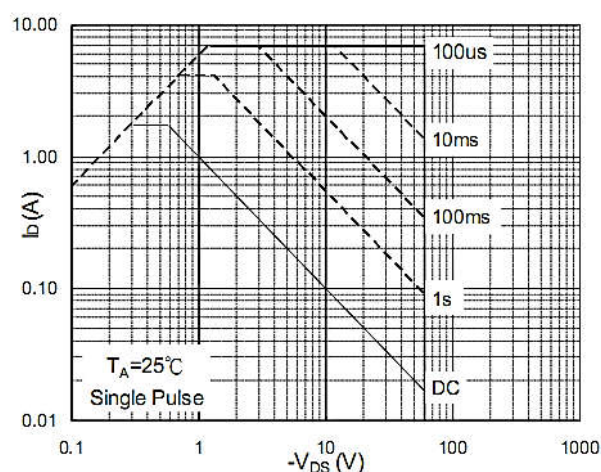


Fig.8 Safe Operating Area

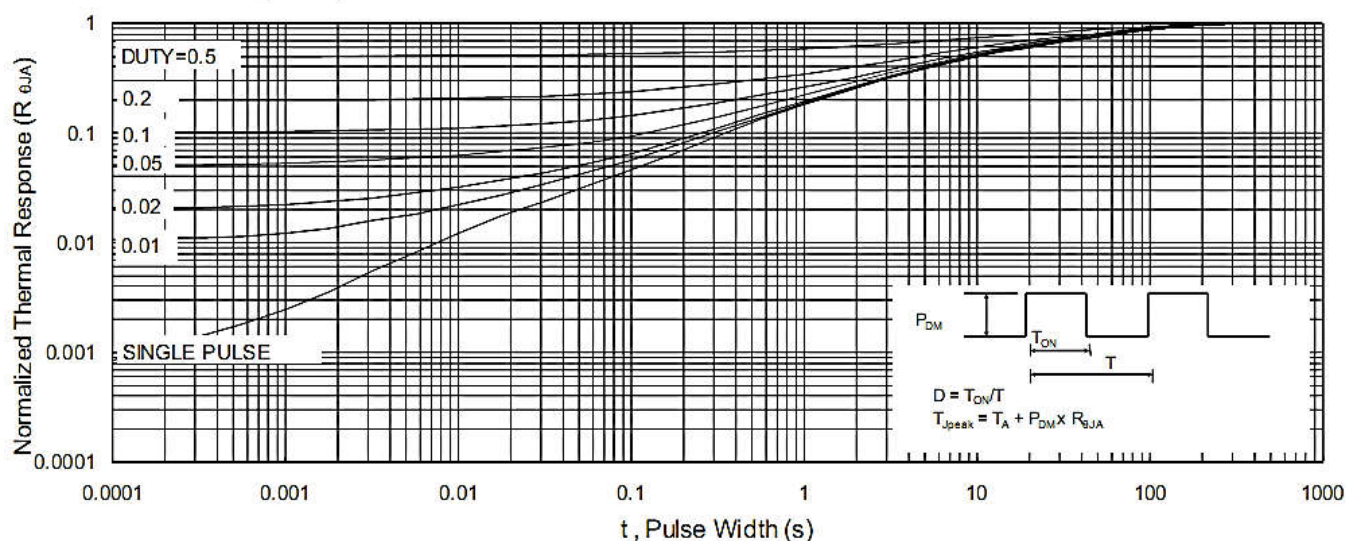


Fig.9 Normalized Maximum Transient Thermal Impedance

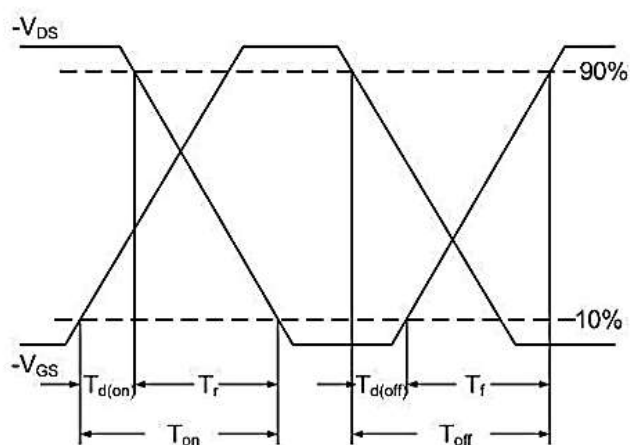


Fig.10 Switching time waveform

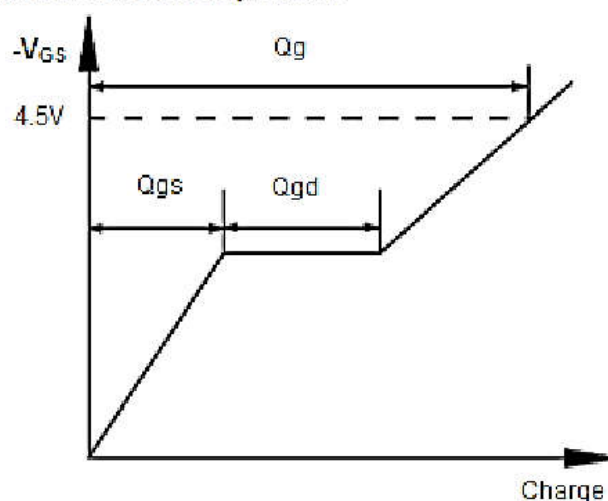


Fig.11 Gate Charge waveform

Ordering information

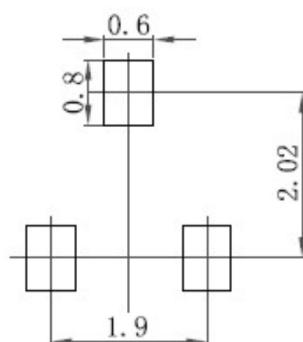
Package	Packing Description	Base Quantity	Packing Quantity
SOT23-3L	Tape/Reel, 7" reel	3000pcs/Reel	24000PCS/Box 120000PCS/Carton

Package Dimensions

SOT23-3L

Dim.	Millimeter (mm)		mil	
	Min.	Max.	Min.	Max.
A	1.05	1.25	41	49.2
A1	0.10		3.93	
A2	1.05	1.15	41	45
b	0.30	0.50	12	20
c	0.10	0.20	3.93	7.9
D	2.82	3.02	111	119
E	1.50	1.70	59	67
E1	2.65	2.95	104	116
e	0.95		37.4	
e1	1.80	2.00	71	78
L	0.30	0.066	12	26
Θ	8°			

The recommended mounting pad size



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