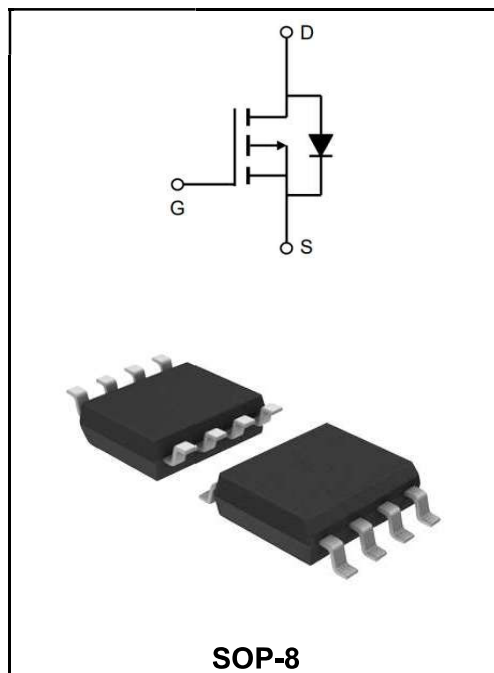


-40V P-CHANNEL ENHANCEMENT MODE MOSFET

MAIN CHARACTERISTICS

I_D	-8A
V_{DSS}	-40V
$R_{DS(on)-typ}(@V_{GS}=-10V)$	< 32mΩ



APPLICATION

- ◆ Battery protection
- ◆ Load switch
- ◆ Uninterruptible power supply

Maximum Ratings at Tc=25°C unless otherwise specified

Characteristics	Symbols	Value	Units
Drain-Source Voltage	V_{DS}	-40	V
Gate - Source Voltage	V_{GS}	±20	V
Continuous Drain Current, V_{GS} @ -10V ¹ @T _A =25°C	I_D	-8	A
Continuous Drain Current, V_{GS} @ -10V ¹ @T _A =70°C	I_D	-6.9	A
Pulsed Drain Current ²	I_{DM}	-32	A
Single Pulse Avalanche Energy ³	E_{AS}	41	mJ
Avalanche Current	I_{AS}	-28.6	A
Total Power Dissipation ⁴ @T _A =25°C	P_D	2.5	W
Storage Temperature Range	T_{STG}	-55 to +150	°C
Operating Junction Temperature Range	T_J	-55 to +150	°C
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	50	°C/W

Maximum Ratings at Tc=25°C unless otherwise specified

Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	BV_{DSS}	-40	-	-	V
BVDSS Temperature Coefficient	Reference to 25°C, $I_D=-1mA$	$\Delta BV_{DSS}/\Delta T_J$	-	-0.02	-	V/°C
Static Drain-Source On-Resistance ²	$V_{GS}=-10V, I_D=-5A$	$R_{DS(ON)}$	-	-	32	mΩ
	$V_{GS}=-4.5V, I_D=-4A$		-	-	46	
Gate -Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	$V_{GS(th)}$	-1.2	-	-2.5	V
$V_{GS(th)}$ Temperature Coefficient		$\Delta V_{GS(th)}$	-	3.72	-	mV/°C
Drain-Source Leakage Current	$V_{DS}=-32V, V_{GS}=0V, T_J=25^\circ C$	I_{DSS}	-	-	-1	μA
	$V_{DS}=-32V, V_{GS}=0V, T_J=55^\circ C$		-	-	-5	
Gate -Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	I_{GSS}	-	-	±100	nA
Forward Transconductance	$V_{DS}=-5V, I_D=-8A$	g_{fs}	-	10.7	-	S
Total Gate Charge(-4.5V)	$V_{DS}=-15V$ $V_{GS}=-4.5V$ $I_D=-1A$	Q_g	-	11.5	-	nC
Gate-Source Charge		Q_{gs}	-	3.5	-	
Gate-Drain Charge		Q_{gd}	-	3.3	-	
Turn-on delay time	$V_{DD}=-15V$ $V_{GS}=-10V$ $I_D=-1A$ $R_G=3.3\Omega$	$t_{d(on)}$	-	22	-	ns
Rise Time		T_r	-	15.7	-	
Turn-Off Delay Time		$t_{d(OFF)}$	-	59	-	
Fall Time		t_f	-	5.5	-	
Input Capacitance	$V_{DS}=-15V$ $V_{GS}=0V$ $f=1MHz$	C_{iss}	-	1415	-	pF
Output Capacitance		C_{oss}	-	134	-	
Reverse Transfer Capacitance		C_{rss}	-	102	-	
Continuous Source Current ^{1,5}	$V_G=V_D=0V$, Force Current	I_S	-	-	-8	A
Diode Forward Voltage ²	$V_{GS}=0V, I_S=-1A, T_J=25^\circ C$	V_{SD}	-	-	-1.2	V

Note :

- 1、The data tested by surface mounted on a 1 inch 2 FR-4 board with 20Z copper.
- 2、The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3、The EAS data shows Max. rating . The test condition is $V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-28.6A$
- 4、The power dissipation is limited by 150°C junction temperature
- 5、The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Ratings and Characteristic Curves

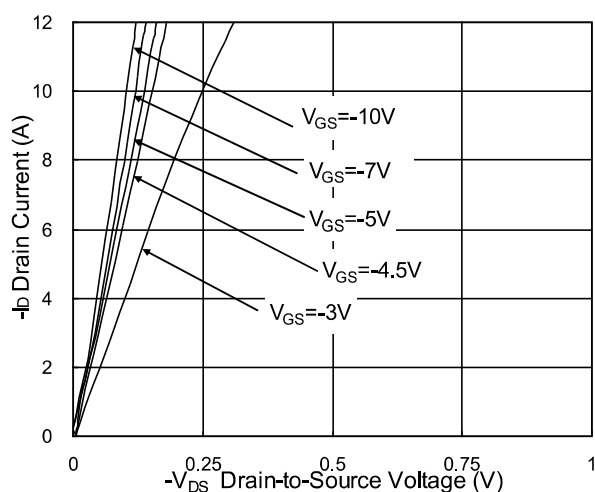


Fig.1 Typical Output Characteristics

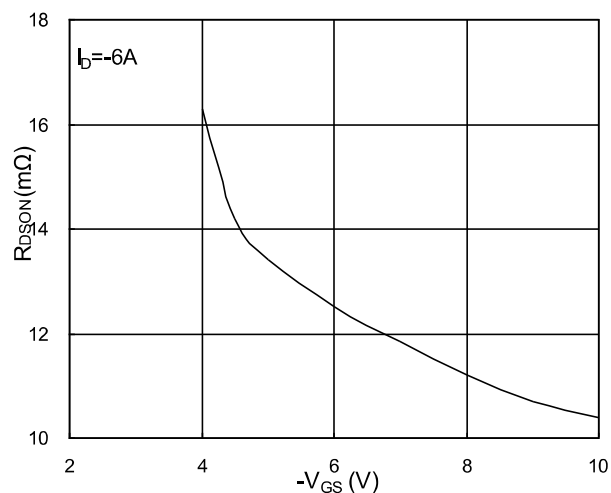


Fig.2 On-Resistance v.s Gate-Source

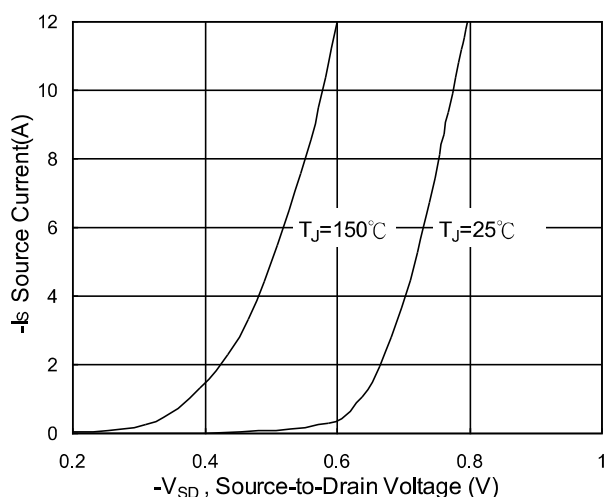


Fig.3 Forward Characteristics Of Reverse

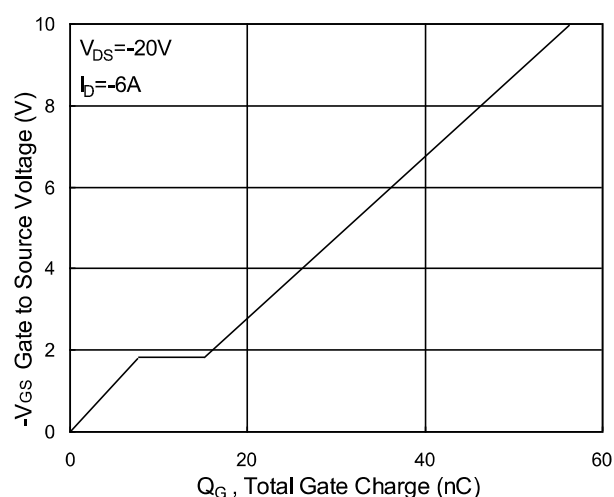


Fig.4 Gate-Charge Characteristics

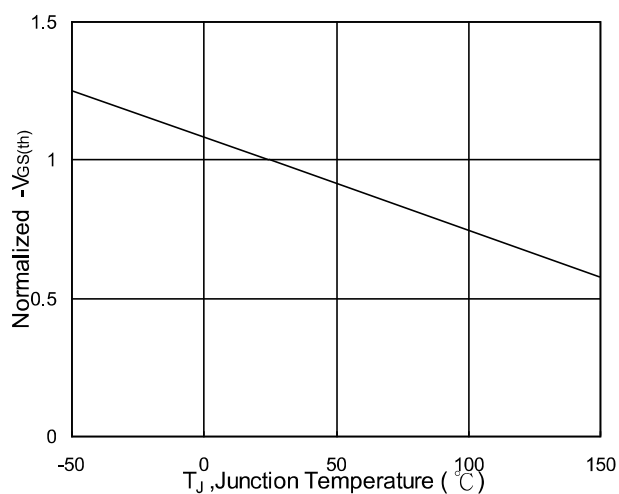


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

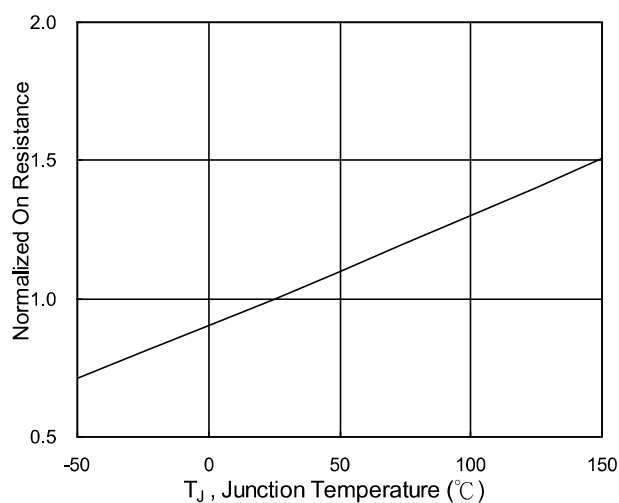


Fig.6 Normalized $R_{DS(on)}$ v.s T_J

Ratings and Characteristic Curves

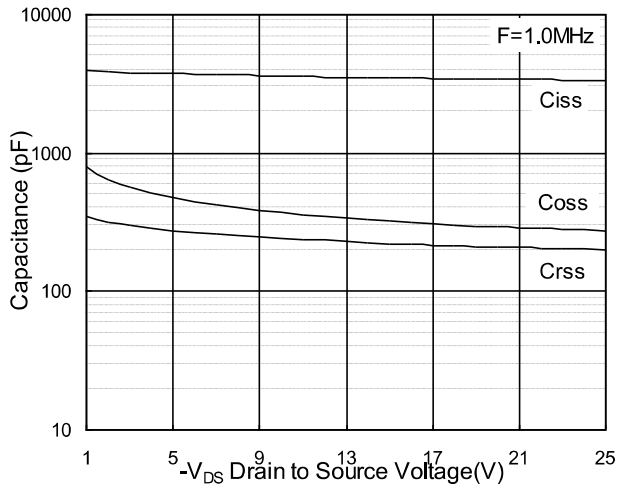


Fig.7 Capacitance

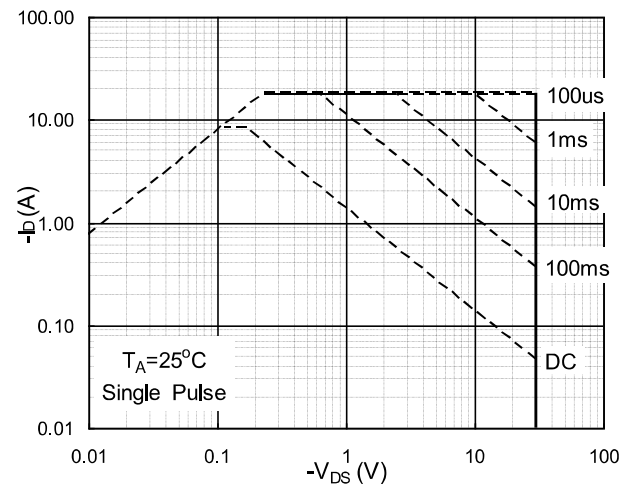


Fig.8 Safe Operating Area

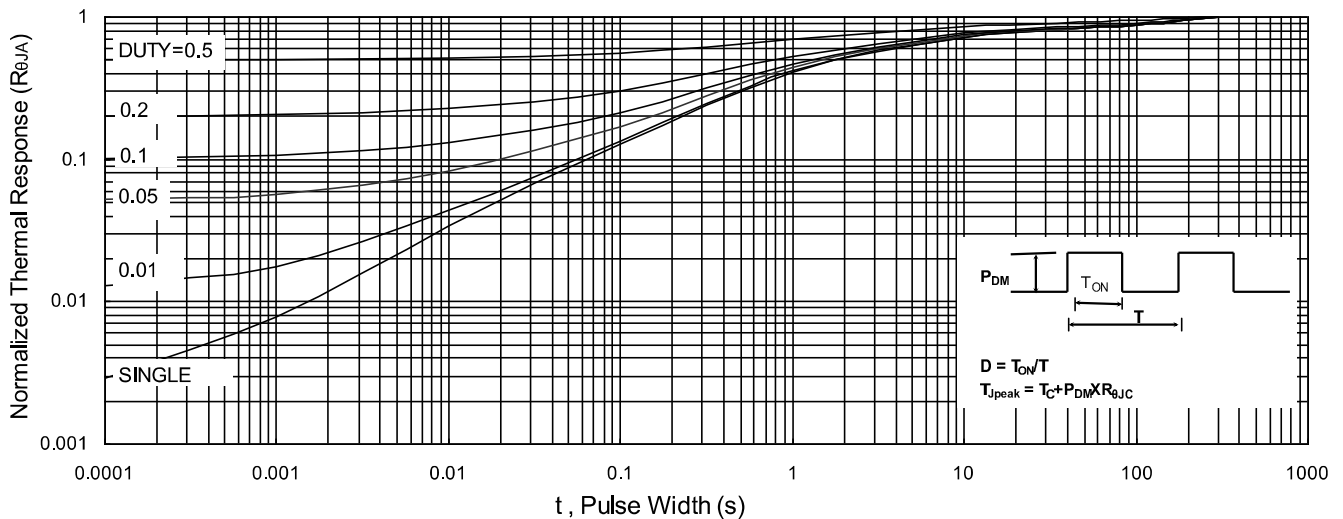


Fig.9 Normalized Maximum Transient Thermal Impedance

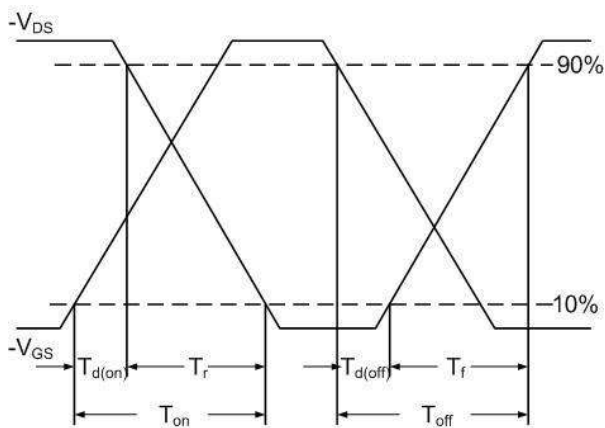


Fig.10 Switching Time Waveform

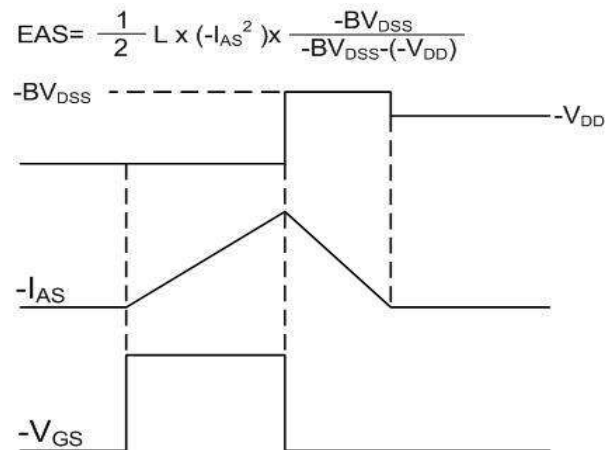
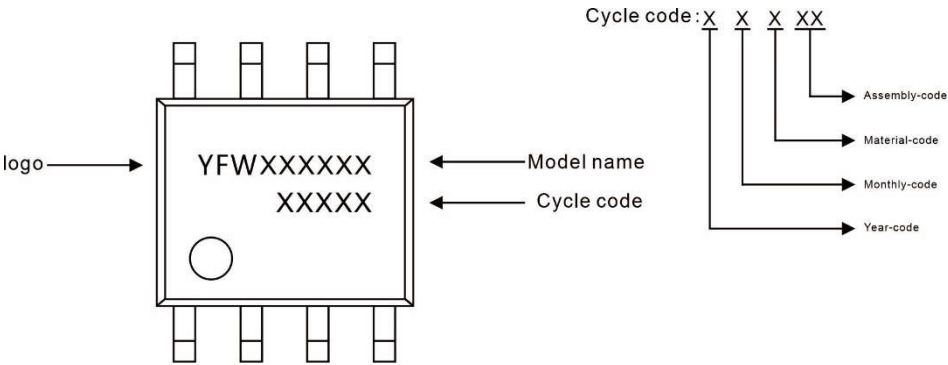


Fig.11 Unclamped Inductive Waveform

Marking Diagram



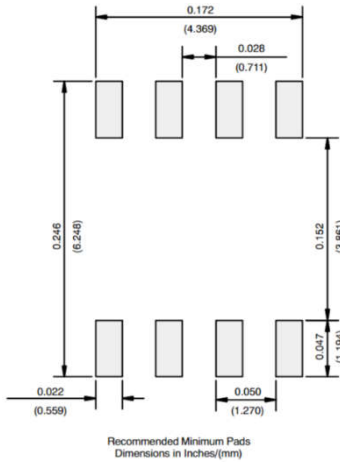
Ordering information

Package	Packing Description	Packing Quantity
SOP-8	Tape/Reel, 13" reel	3000PCS/Reel 30000PCS/Carton

Package Dimensions
SOP-8

Dim	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
A2	1.35	1.50	0.053	0.059
b	0.35	0.55	0.014	0.022
c	0.15	0.25	0.006	0.010
D	4.80	5.00	0.189	0.197
D1	3.10	3.50	0.122	0.138
E	5.80	6.20	0.228	0.244
E1	3.80	4.00	0.150	0.157
E2	2.20	2.60	0.087	0.102
e	1.27 (BSC)		0.050 (BSC)	
L	0.40	1.27	0.016	0.050
θ	0°	8°	0°	8°

The recommended mounting pad size



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